

GP. 2818
2700
[2885/10] #6
2781 MMV
12-13-98

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

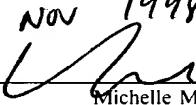
Applicants : VORBACH et al.
Serial No. : 08/947,254
Filed : October 8, 1997
For : I/O MEMORY BUS SYSTEM FOR DFPs AND UNITS
WITH TWO- OR MULTI-DIMENSIONAL
PROGRAMMABLE CELL ARCHITECTURES
Art Unit : 2818-2700

Assistant Commissioner
for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the
United States Postal Service as first class mail in an envelope addressed
to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

Date: 19 Nov 1998

Reg. No. 36,098

Signature: 

Michelle M. Carniaux

RECEIVED

DEC 01 1998

GROUP 2100

COMMUNICATION

SIR:

Applicant wishes to bring to the attention of the Examiner the following co-pending applications:

U.S. Serial No. 08/544,435
U.S. Serial No. 08/946,810
U.S. Serial No. 08/947,002
U.S. Serial No. 09/000,724
U.S. Serial No. 08/946,998
U.S. Serial No. 08/946,999
U.S. Serial No. 09/145,139
U.S. Serial No. 08/946,812

Received

DEC 07 1998

Group 2700

It is requested that each of these applications be considered and made of record herein.

Respectfully submitted,

KENYON & KENYON

By: 

Michelle M. Carniaux (Reg. No. 36,098)

Dated: 19 Nov 1998

KENYON & KENYON
One Broadway
New York, NY 10004
(212) 425-7200 (telephone)
(212) 425-5288 (facsimile)